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Fundamentals of Digital Logic with VHDL Design, 3rd

He is a coauthor of two other books: Fundamentals of Digital Logic with VHDL Design and Field-Programmable Gate Arrays. Zvonko Vranesic received his B.A.Sc., M.A.Sc., and Ph.D. degrees, all in Electrical Engineering, from the University of Toronto.

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... The logic level must remain on the D input for a time equal to or greater than t_{th} after the triggering edge of the clock pulse for reliable data entry. Thomas L. Floyd

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Digital Logic Design BiBasics Combinational Circuits Sequential Circuits Pu-Jen Cheng Adapted from the slides prepared by S. Dandamudi for the book, Fundamentals of Computer Organization and Design.

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1 Fundamentals of Digital Logic Design ECE/CS 3700 Spring 2018, Homework # 3 Due Date: Tuesday, Feb 27, 5pm sharp, due in the HW locker. At 5pm, I will upload solutions to this HW, so that you will have ample time to prepare for the upcoming Mid-Term exam.

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